Hoehenkirchen-Siegertsbrunn
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TRACE32® supports EnSilica eSi-RISC CPU family

Lauterbach, the provider of world class debug tools, has announced support for the eSi-RISC family of microcontrollers from EnSilica Inc. Initial tests on the eSi-3200 were very successful and further cores will be added soon.

Stephan Lauterbach, General Manager of Lauterbach GmbH says: "Support of eSi-RISC in TRACE32 brings for EnSilica's customers not only the state-of-the-art debugging experience, but also opens the way to leverage integrations with real-time operating systems. Companies use TRACE32 with many different processor architectures and can now align debugging of eSi-RISC with other processor architectures. "

Tobias Veerkamp, Firmware designer from Allied Vision Technologies GmbH and one of the first customers says "Reproduction of error states in the camera firmware is not always easy, and when you finally got there, the debugger must give you all the information you need without any issues. TRACE32 brings us great reliability. And there are, of course, the usual advantages of 'taking Lauterbach', including easy debugging of our multicore architecture, comprehensive view of peripheral devices and full awareness of the ThreadX operating system.

EnSilica is a privately owned and funded IC design house with a consistent record of financial growth and technical excellence since its formation in 2001. eSi-RISC is a highly configurable microprocessor architecture for embedded systems, that scales across a wide range of applications. The RISC IP core has been silicon proven in a number of ASIC and FPGA technologies.

Lauterbach GmbH is the world largest provider of debug tools with more than 35 years' experience in making world class debuggers and real-time traces.

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