

TRACE32® and Timing-Architects bring together Hardware Trace Based Verification for Multi-core Systems

Hoehenkirchen-Siegersbrunn and Regensburg, February 2015 – Timing-Architects Embedded Systems GmbH, a high-tech company specialized on the evaluation and optimization of embedded multi- and many-core real-time systems, and Lauterbach GmbH, the leading manufacturer of microprocessor development tools, today announced the integration of Lauterbach TRACE32® with the Timing-Architects (TA) Tool Suite.

The integration allows the import of hardware traces of single- and multi-core applications recorded by TRACE32 into the TA Inspector. The TA Tool Suite can then be used to detect errors in the application, validate requirements and help the migration from single-core to multi-core projects by creating a timing model. This workflow leads to an overall improvement in quality and safety of embedded multi-core applications.

TRACE32 can record non-intrusive operating-system aware real-time traces of embedded multi-core applications without altering the run-time behavior of the application. TRACE32 extracts the timing behavior of the system, including the task states and function runtimes from the recorded trace data and exports it into a CSV format which can then be used by the TA Inspector. The TA Inspector is able to import multiple CSV files in parallel, which means an arbitrary amount of processor cores are supported.

"Lauterbach GmbH is delighted about the close cooperation with Timing-Architects Embedded Systems GmbH and the benefits for our mutual customers, who get a highly sophisticated combination of tools. The new seamless integration of Lauterbach TRACE32 with the TA Tool Suite forms a powerful tool chain to model, analyze and optimize the run-time behavior of time critical multicore-based applications. This tool combination is a perfect solution for statistical timing analysis and model back-annotation of runnables and functions on AUTOSAR operation systems." said Stephan Lauterbach, managing director of Lauterbach.

"Our customers see tracing technique for runtime measurement and target verification as one fundamental part of the multicore development process. With the interface to Lauterbach TRACE32 and tracing hardware we are very pleased to provide customers an integrated solution which works closely together and enables a closed-loop model-based development process. This reduces much manual effort at verification and modeling and helps engineers to migrate software from single-core to multi-core platforms." states Dr. Michael Deubzer, CEO of Timing-Architects Embedded Systems GmbH.

Visit Lauterbach and TA at Embedded World 2015 in Nuremberg

The integration of Lauterbach TRACE32 and Timing-Architects Tool Suite will be presented at Embedded World 2015 from February 24th to 26th. In Nuremberg you can test the trace import features yourself and come and see experts from Lauterbach and Timing-Architects to ask further questions. You can find Lauterbach in **hall 4-booth 210** and TA in **hall 4-booth 378**.

About Lauterbach

Lauterbach is the leading manufacturer of complete, modular and upgradeable microprocessor development tools worldwide with experience in the field of embedded designs since 1979.

It is an international, well-established company with blue chip customers in every corner of the globe and has a close working relationship with all semiconductor manufacturers. At the headquarters in Höhenkirchen, near Munich, the engineering team develops and produces highly proficient and specialized Development Tools, which are utilized all over the world under the brand TRACE32®. Own branch offices exist in United Kingdom, Italy, France, Tunisia, on the East and West coasts of the United States, Japan and China. Highly qualified sales and support engineers are also available in many other countries. For more information visit <http://www.lauterbach.com/>

About Timing-Architects

Timing-Architects (TA) is an internationally operating high-tech company, specialized in the optimization of software for embedded multi- and many-core real-time systems. For their innovative tool solution "TA Tool Suite" the company received multiple awards and is also recognized by experts as a leading player in research and development of multi-core embedded systems. Timing-Architects Tool Suite covers the system design phase, simulation and analysis, architecture and module development, as well as target verification. Due to a connection of all modules, a closed loop solution is provided for the whole development process. With TA Designer, TA Simulation, TA Optimizer, and TA Inspector, Timing-Architects provides tools to support project managers, architects, developer, integration and test engineers to broaden the performance and increase the efficiency of their multi-core projects.

For more information visit www.timing-architects.com

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