

TRACE32 Product Training

All training will be individually prepared to meet the specific needs of the group involved. A standard version of the agenda is provided on page two.

The format that we have found to be the best comprises a running demonstration with the main aspects interlinked to a powerpoint summary. A hard copy of the presentation will be provided so that each attendee has a means of taking notes and then linking them to the features shown. It will also provide a reference manual for later study if required.

The standard course is based upon the attendees having prior experience in embedded design and is intended to familiarise them with the unique features and capabilities of the Lauterbach product range.

The optimum size for a group is between four and ten people. This is to allow the necessary interaction with the trainer and to provide the individual attention needed so that the attendees gain the maximum benefit.

Groups outside this size range and alternative formats can be catered for upon request.

Training at the UK office in Basingstoke, Hampshire

A one day course with a buffet lunch and appropriate refreshments can be arranged at our offices.

for a group of up to 6 people	£ 1800
for each additional person	£ 300

(the absolute max number is 12)

Training on customer site in the UK or Eire

A one day course – the customer to provide the conference room and refreshments. The costs include travel and accommodation as needed by the trainer.

for a group of up to 6 people	£ 2200
for each additional person	£ 300

Set up and quick start program

For customers who have purchased tools and wish to have our field applications engineer on site for a day to provide a brief course on how to setup and configure the tools for your application followed by a demonstration of the tools. Note that as the possible variations of system and projects are infinite we do not undertake to fully configure your system in one day but we can dramatically shorten your timescales and learning curve.

one day on site consultancy	£ 1000
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Standard Agenda

Overview

Jtag concept, capabilities and your processor limitations
or
Emulators design concept, connections and technical capabilities

Demonstration/operation of Lauterbach Jtag debuggers

setup
configuration
softkeys and menus
cpu settings
control, run/stop/step modes
breakpoints
 hard/soft/spot
 complex breakpoints
registers
onchip peripherals
runtime performance measurement

Dependant upon processor architecture all or none of the following

trace
 data/ timing / reconstruction
on-chip trace analysis
code coverage

Dependant upon project needs any of the following

power probe (a low cost, easy use, logic analyzer)
VHDL scanner
SoC multi core debugging
multi chip debugging

The above agenda will be tailored to provide the appropriate emphasis needed for your project and to suit the experience level of the development team.

TRACE32[®]

TRACE32-ICD

Request for quotation for training

Company name and site address

Contact name
 tel
 fax
 e.mail

Course to be held on your site
in Basingstoke

proposed number of attendees

system microprocessor type

any other aspects of the design that
are relevant e.g. FPGAs, multicores

Any special needs or a customised agenda
request

Preferred dates
if possible provide a choice of 3

CONTACT

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